



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Wang

Serial No.: 09/916,619

Group Art Unit: unknown

Filed: July 26, 2001

Examiner: not assigned

Title: Self Aligned Method Of Forming A Semiconductor Memory Array Of Floating Gate Memory Cells With Control Gate Spacers, And A Memory Array Made Thereby

POWER OF ATTORNEY BY ASSIGNEE

Assistant Commissioner for Patents  
Washington, DC 20231

Sir:

The undersigned, being Assignee of the entire interest in the above-identified application by Assignment dated September 4, 2001, which assignment is ☐ recorded on \_\_\_\_\_ under Reel \_\_\_\_\_ and Frame \_\_\_\_\_, ☒ copy attached hereto, under 37 C.F.R. §3.71, to prosecute the application to the exclusion of the Inventor(s), and grants Power of Attorney and correspondence address as stated herein.

Silicon Storage Technology, Inc., hereby appoints:

Barry N. Young (Reg. No. 27,744); Timothy W. Lohse (Reg. No. 35,255); Steven R. Sprinkle (Reg. No. 40,825); Terrance A. Meador (Reg. No. 30,298); Ramsey R. Stewart (Reg. No. 38,322); June M. Learn (Reg. No. 31,238); John Oskorep (Reg. No. 41,234); Timothy N. Ellis (Reg. No. 41,734); William G. Goldman (Reg. No. 42,590); Sheila Kirschenbaum (Reg. No. 44,835); Travis L. Dodd (Reg. No. 42,491); Charles D. Gavrilovich, Jr. (Reg. No. 41,031); Gerald W. Maliszewski (Reg. No. 38,054); Hayward A. Verdun (Reg. No. 43,223); Armando Pastrana, Jr. (Reg. No. 44,997); Richard M. Goldman (Reg. No. 25,585); Lisa A. Haile (Reg. No. 38,347); Sal Lim (Reg. No. 45,706); Joseph R. Baker, Jr. (Reg. No. 40,900); Richard J. Imbra (Reg. No. 37,643); Mark L. Berrier (Reg. No. 35,066); Mark M. Takahashi (Reg. No. 38,631); James P. Cleary (Reg. No. 45,843); Karl A. Limbach (Reg. No. 18,689); Gerald T. Sekimura (Reg. No. 30,103); Kyla L. Harriel (Reg. No. 41,816); Eric N. Hoover (Reg. No. 37,355); George C. Limbach (Reg. No. 19,305); Ronald L. Yin (Reg. No. 27,607); Alan A. Limbach (Reg. No. 39,749); Edward B. Weller (Reg. No. 37,468); Peter R. Leal (Reg. No. 24,226); David Alberti (Reg. No. 43,465);  
of

Gray Cary Ware & Freidenrich LLP  
Attn: Patent Department  
1755 Embarcadero Road  
Palo Alto, California 94303-3340

as attorneys/agents with full power of substitution and revocation to prosecute this application  
and to transact all business in the Patent and Trademark Office connected therewith.

Assignee:

Silicon Storage Technology, Inc.

Date: Sep. 4, 2001

Printed Name:

Title:

BING YEH

PRESIDENT & CEO



ASSIGNMENT

WHEREAS, I, Chih Hsin Wang, hereinafter referred to as "ASSIGNOR", have invented certain new and useful improvements as described and set forth in the below-identified application for United States Letters Patent:

Title of Invention: SELF ALIGNED METHOD OF FORMING A SEMICONDUCTOR MEMORY ARRAY OF FLOATING GATE MEMORY CELLS WITH CONTROL GATE SPACERS, AND A MEMORY ARRAY MADE THEREBY

Application (Declaration/Oath) Execution Date: August 31, 2001

Filing Date: July 26, 2001

Application No.: 09/916,619

WHEREAS, Silicon Storage Technology, Inc., a corporation of California, 1171 Sonora Court, Sunnyvale, California 94086 hereinafter referred to as "ASSIGNEE", is desirous of acquiring the entire right, title and interest in the said invention and application and in any Letters Patent which may be granted on the same;

NOW, THEREFORE, TO ALL WHOM IT MAY CONCERN: Be it known that, for One Dollar (\$1.00) and other good and valuable consideration, receipt of which is hereby acknowledged by Assignor, Assignor has sold, assigned and transferred, and by these presents does sell, assign and transfer unto the said Assignee, and Assignee's successors and assigns, all right, title and interest in and to the said invention, said application for United States Letters Patent, and any Letters Patent which may hereafter be granted on the same in the United States and all countries throughout the world including any divisions, renewals, continuations in whole or in part, substitutions, conversions, reissues, prolongations or extensions thereof, said interest to be held and enjoyed by said Assignee as fully and exclusively as it would have been held and enjoyed by said Assignor had this assignment and transfer not been made, to the full end and term of any such Letters Patent.

Assignor further agrees that he/she will, without charge to said Assignee, but at Assignee's expense, cooperate with Assignee in the prosecution of said application and/or applications, execute, verify, acknowledge and deliver all such further papers, including applications for Letters Patent and for the reissue thereof, and instruments of assignment and transfer thereof, and will perform such other acts as Assignee lawfully may request, to obtain or maintain Letters Patent for said invention and improvement in any and all countries, and to vest title thereto in said Assignee, or Assignee's successors and assigns.

COPY

IN TESTIMONY WHEREOF, Assignor has hereunto signed his/her name to this assignment on the date indicated below.

Date: 9/4/01

Chih Hsin Wang  
Chih Hsin Wang

State of California

County of Santa Clara

On September 4, 2001 before me, Grace Fagalde, personally appeared Chih-Hsin Wang personally known to me or proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.



WITNESS my hand and official seal.

Grace Fagalde  
Signature of Notary

SEAL